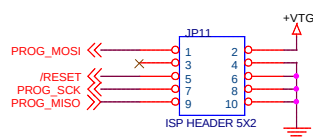
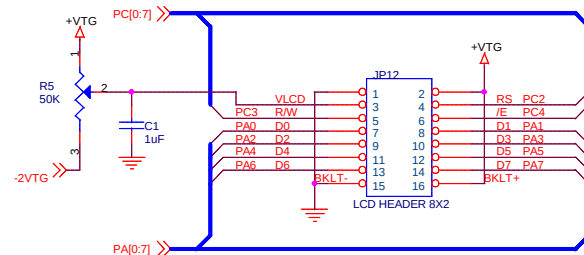


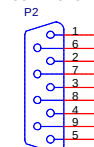
ISP HEADER



HD44780 CHARACTER LCD INTERFACE

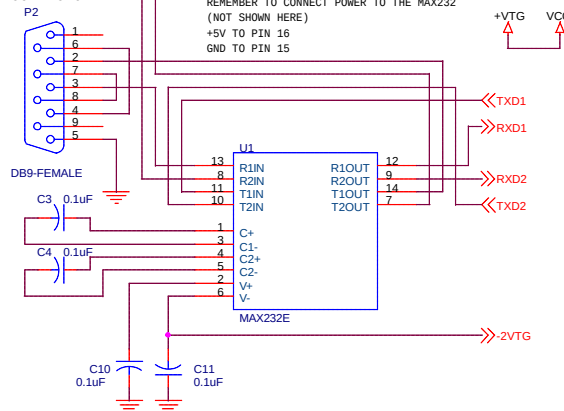


SERIAL CONNECTOR

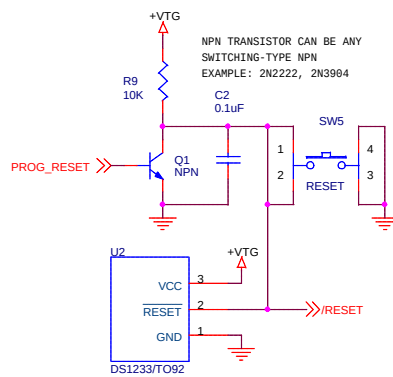


SERIAL PORT/DEBUG INTERFACE

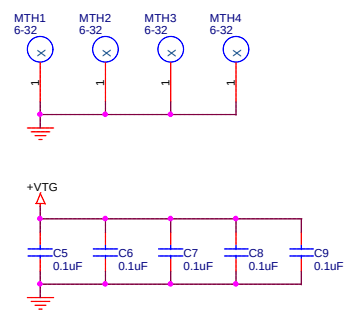
REMEMBER TO CONNECT POWER TO THE MAX232
(NOT SHOWN HERE)
+5V TO PIN 16



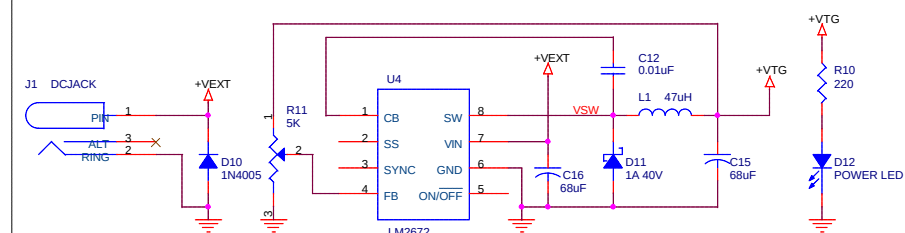
CPU RESET CONTROL



MOUNTING HOLES AND BYPASS CAPACITORS

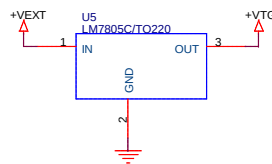


POWER JACK AND SWITCHING REGULATOR

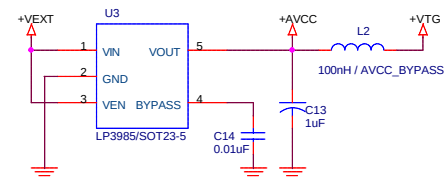


ALTERNATE LINEAR REGULATOR

LINEAR REGULATOR MAY REQUIRE
HEATSINKING IF INPUT VOLTAGE
IS HIGH



ANALOG SUPPLY LINEAR REGULATOR



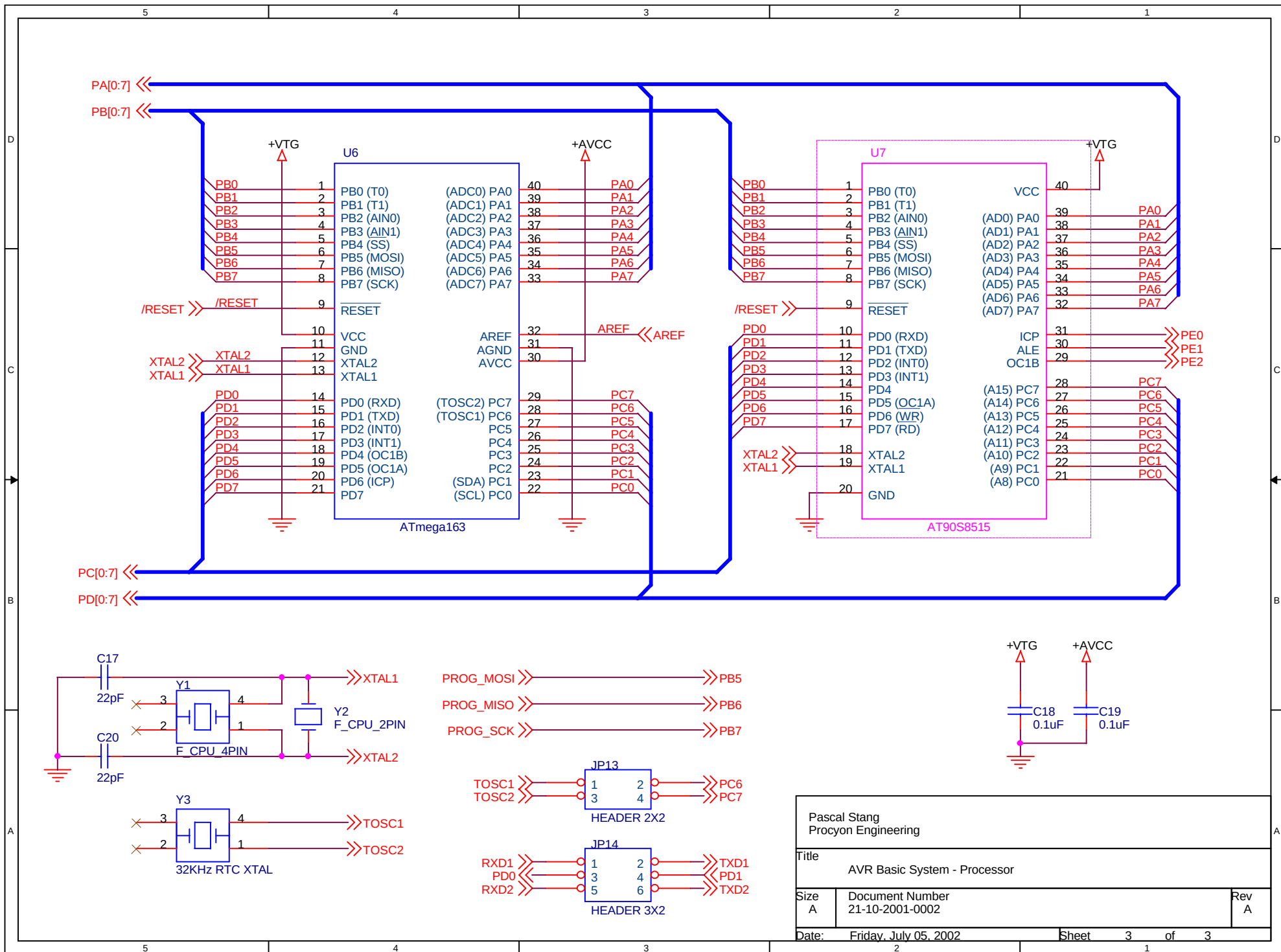
Pascal Stang
Procyon Engineering

Title	AVR Basic System - Interface
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Size B	Document Number 21-10-2001-0002
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Date: Friday, July 05, 2002

Rev
A



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Title AVR Basic System - Processor		
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